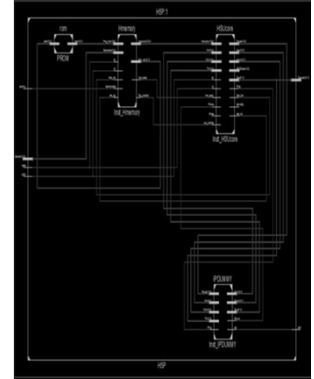


# LE PROCESSEUR HOMADE

## Le cœur Homade

- UltraRISC processor
  - 11 instructions
- It can do nothing alone
- You can add 2048 IPs for your needs

**Smaller = Bigger**



New 3D technologies arriving  
2,5D FPGA: virtex 7

New reconfigurable path could become parallel between 2 dies

Modele of computation has to be ready for that!!

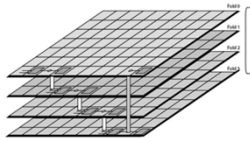
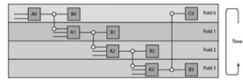
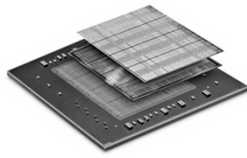
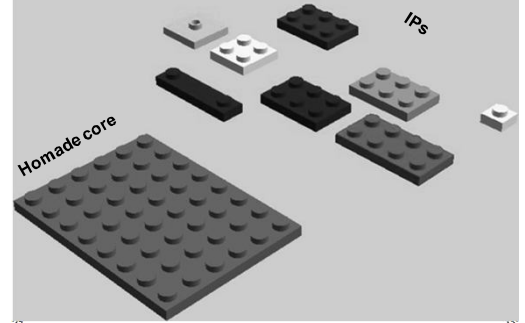


Figure 1. Typical approach to split the logic into one or more dies. Each die uses the same die stack, and the FPGA fabric changes each die. Dies are connected via a 2.5D approach (see Figure 2). This is a reconfigurable path for each die, allowing for the die to be reconfigured to match the logic within a die or the next die. The top die block is the die stack (virtex 7).

21

VHDL Pieces of code



21

22

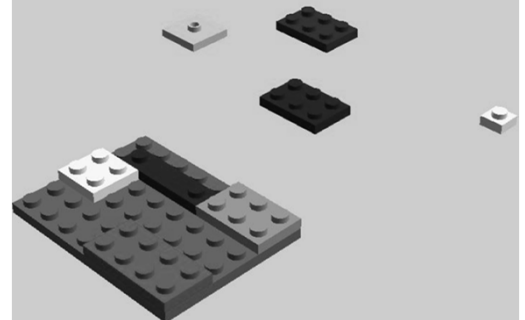
- Model of computation
  - => Hardware programming
  - => Software programming
- Parallel hardware when you need it!
  - => Partial reconfiguration must be parallel
- Dynamicity + complexity
  - => Verifications



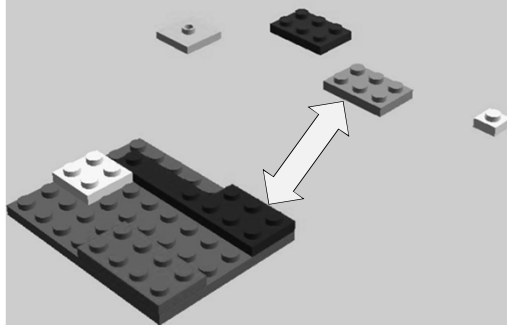
21

22

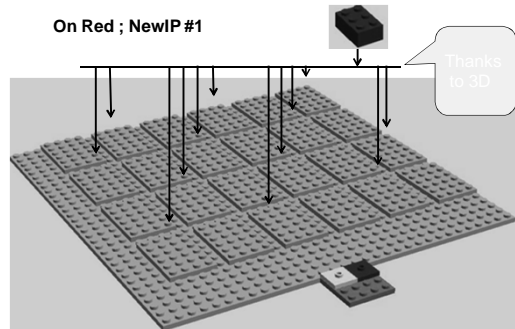
Ready to run...



### Dynamic reconfiguration of IP

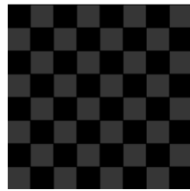


On Red ; NewIP #1

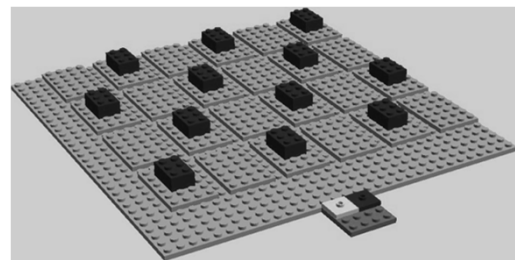


### Toward MSPMD model of computation and architecture

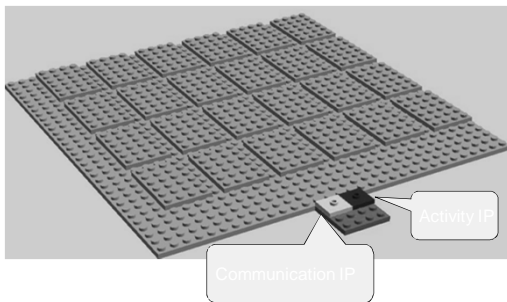
- Specific IP and instructions
  - Programmable dynamicity
  - Call SPMD / ORTREE
- MSPMD ex:
  - On Red -- IPON
  - NewIP #1
  - SPMD #1
  - On black
  - NewIP #2
  - SPMD #2
  - On all
  - ORTREE



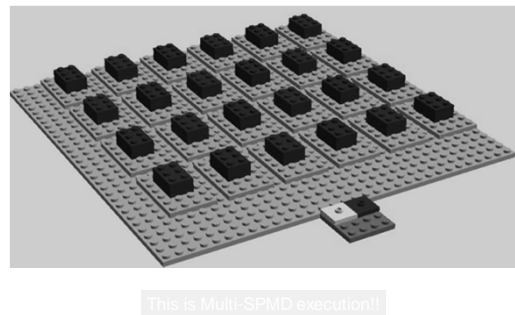
SPMD #1



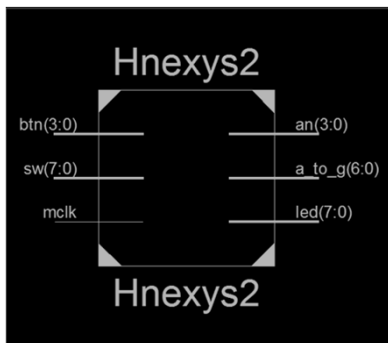
6 x 4 Homades + Master Homade LEGO prototype



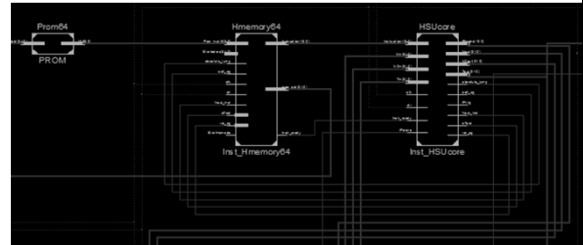
On Black ; NewIP #2, SPMD #2



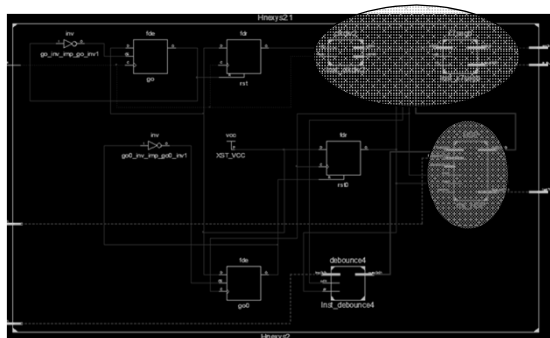
## Homade sous ISE



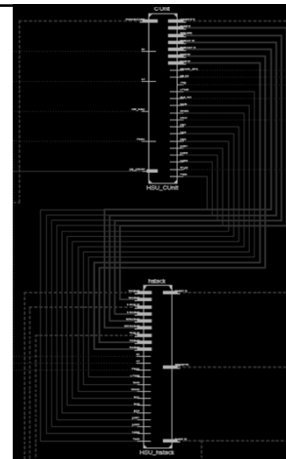
## La structure Homade



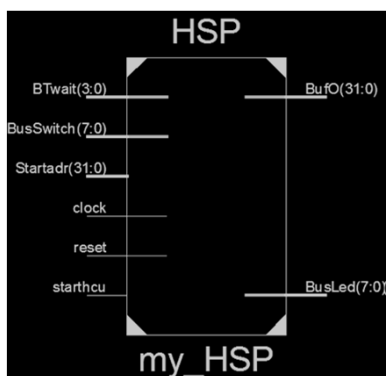
## Inside nexys2 ou 3



## Le HSU



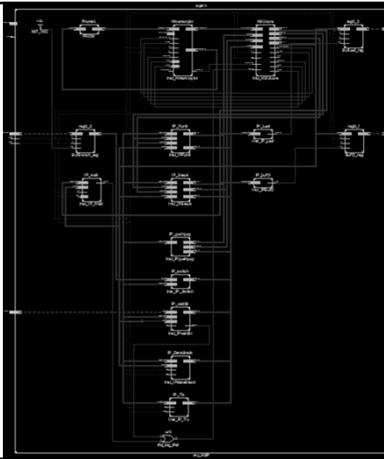
## Le cœur Homade



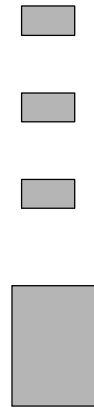
## La stack



Avec nos  
Ips!!!



Pop 0  
push 0



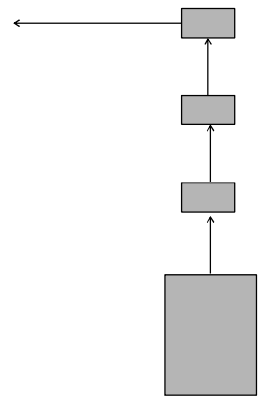
## Le jeu d' instructions

- Toutes les instructions sont codées sur des mots de 16 bits
- instructions étendues : 3 mots de 16 bits
  - Elles sont alignées sur des mots de 64 bits



- L'instruction de remplissage est codée '1\_11\_11\_1\_111111111'

1 0



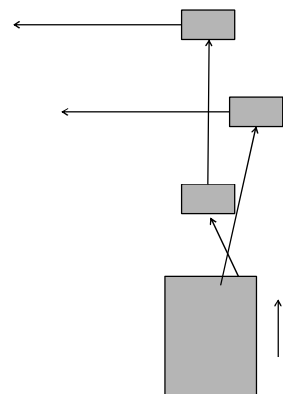
## Instruction IP

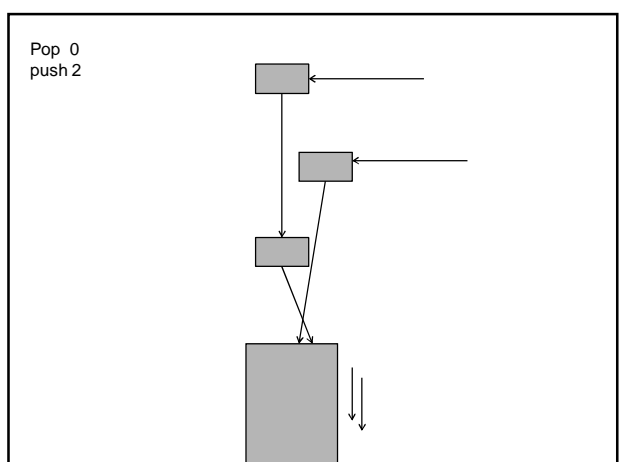
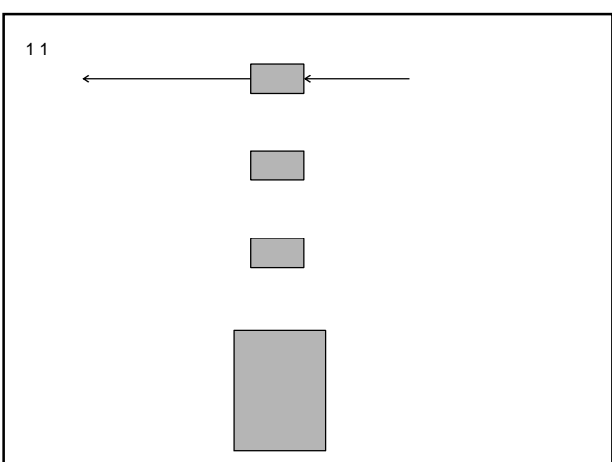
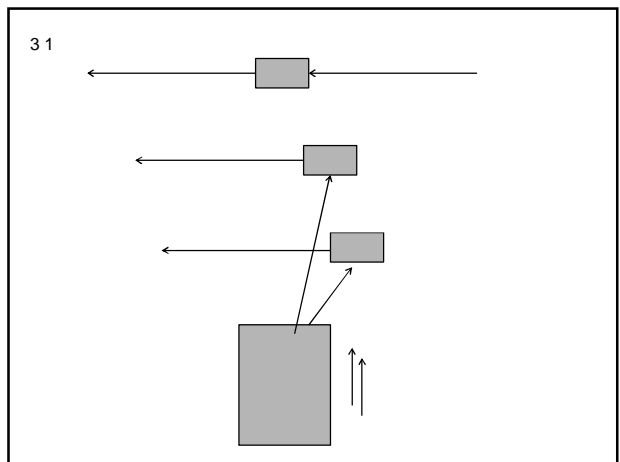
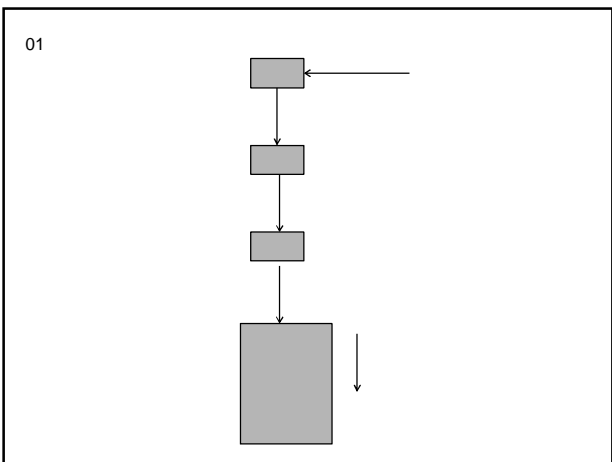
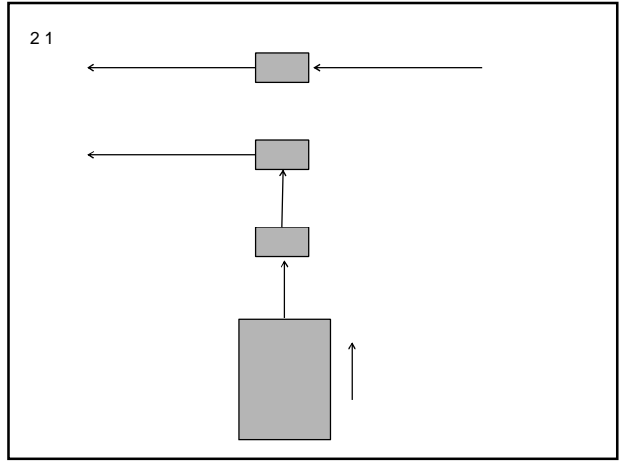
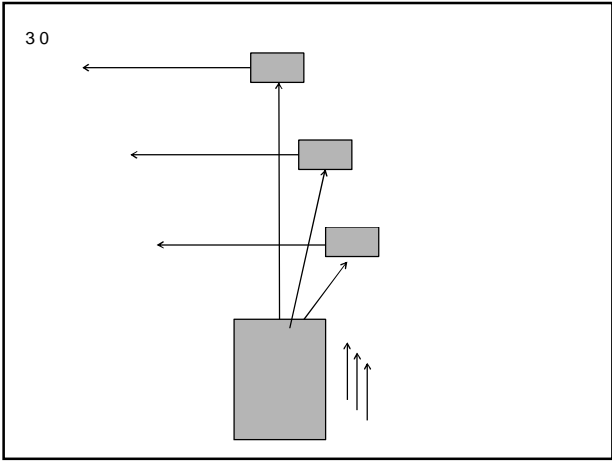
- Son format sur 16 bits :

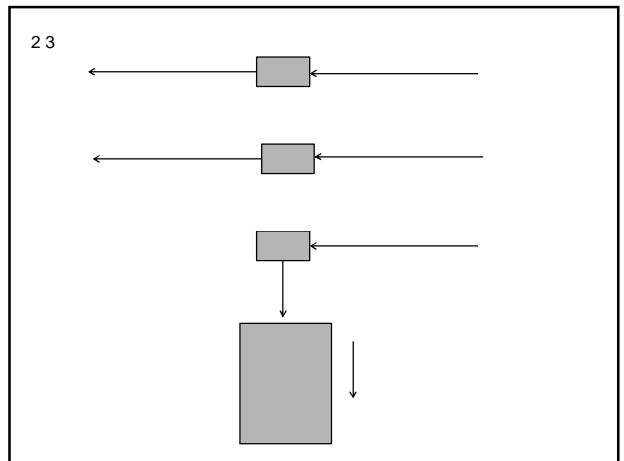
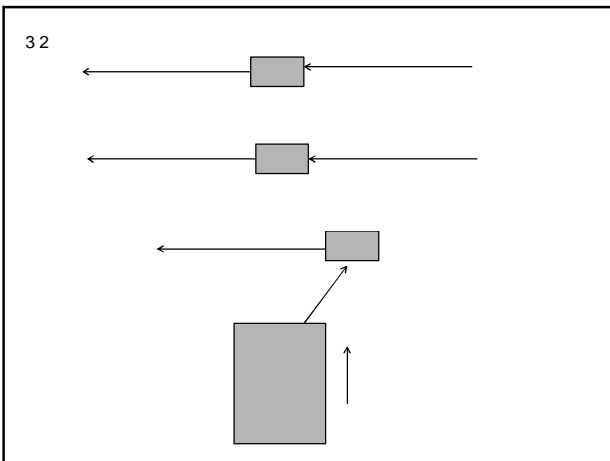
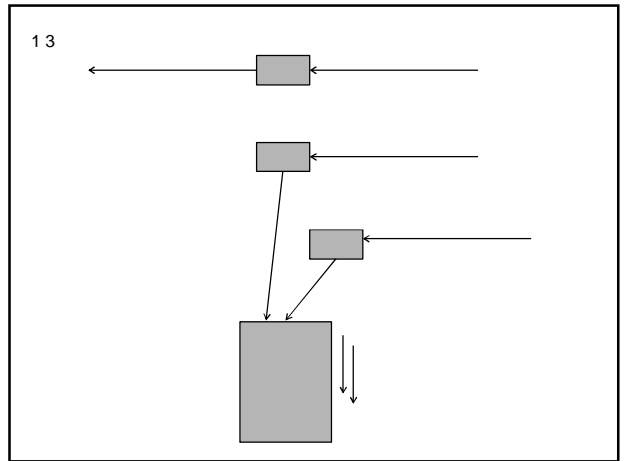
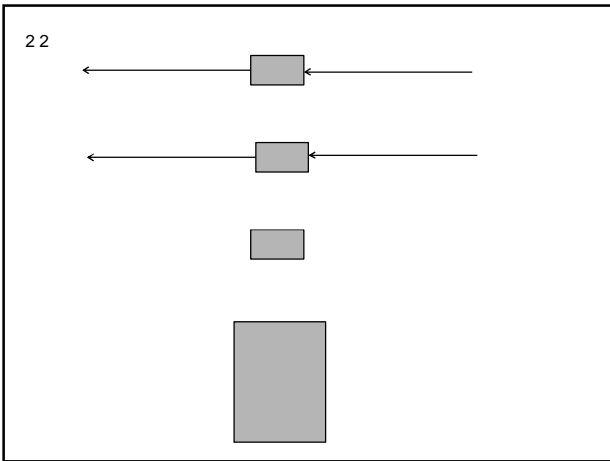
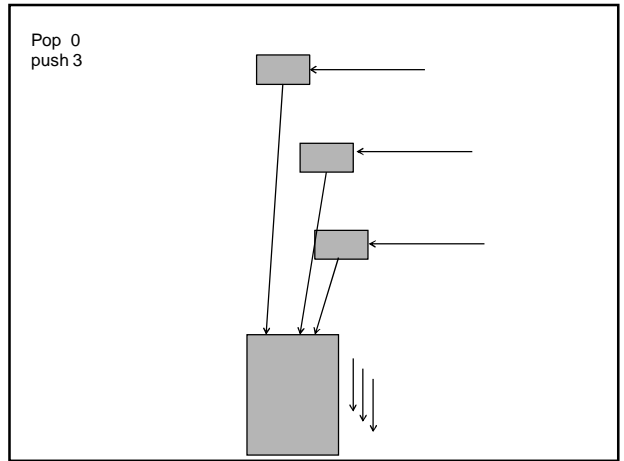
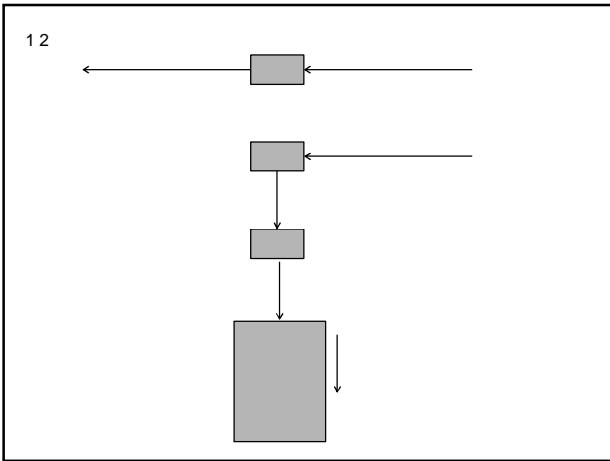


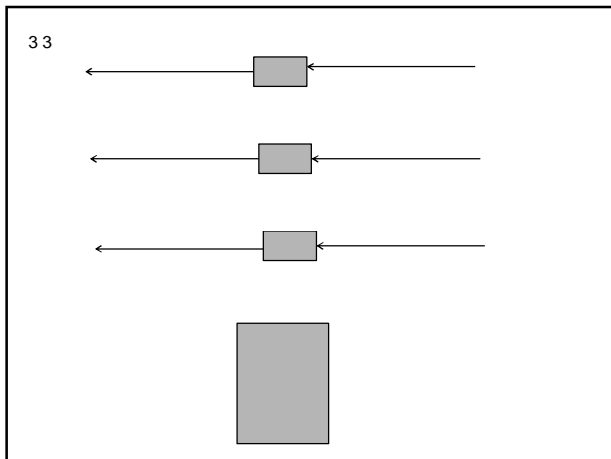
- XX : Pop 0, 1, 2 ou 3 valeurs retirées de la pile vers l'IP
- YY : Push 0, 1, 2 ou 3 valeurs envoyées par l'IP sur la pile
- S :
  - '0' indique un IP qui s'exécute en moins d'un cycle : Short\_IP.
  - '1' signifie que l'IP s'exécute sur plus de 1 cycle : Long\_IP. signal IPdone est déclenché par l'IP pour signaler la fin d'exécution.
- I I I I I I I I I I : sur 10 bits, le numéro de l'Ip à déclencher. On a donc 1024 IP en un cycle et 1024 IP en plus de un cycle.

2 0









### Instruction Call

- Idem que BRA, mémorise l'adresse de retour dans l'entity mémoire

0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F
f	f	f	f	f	f	f	f	f	f	f	f	f	f	f	f

### Le contrôle de flot

10 bits pour le déplacement.  
Ce déplacement est un entier en complément à deux qui s'ajoute à la valeur du compteur ordinal pointant sur cette instruction de branchement

- Branchement relatif BR**

0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

- Branchement relatif si Zero BZ**

0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

- Branchement relatif si Non Zero BNZ**

0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

- Return permet le retour d'appel de procédure par restauration du compteur ordinal

0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

- Halt permet d'arreter le processeur dans l'état courant

0	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

### Branchement absolu

- range la valeur codées sur les deux mots de 16 bits suivants dans le CO

0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0
F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F
f	f	f	f	f	f	f	f	f	f	f	f	f	f	f	f

### Les itérations

- LIT permet de manipuler des nombres en complément à deux sur 12 bits.

0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

- Plusieurs LIT de suite avec des decalages et AND logique permettent de construire des mots de plus de 12 bits : il faut instancier l'IP!!

## Gestion du parallélisme

- SPMD déclenche un programme sur tous les Homade connectés à une adresse explicite

```
0 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1
```

- Adresse de début 'IIIIIIIIII00' sur 16 (15) bits
- Wait : barrière de synchro entre tous les Homade connectés

```
0 1 1 0 0 0 0 0 0 0 0 0 0 0 0 0
```

## La syntaxe de l'assembleur

- Homadeasm :=  
 <ID\_list>  
 Program  
 [ [<function\_decl\_list>] begin]  
 <instruction\_list>  
 endprogram
- ID\_list:= VARIABLE <id\_Function>
- function\_decl := <id\_Function> FUNCTION  
 <instruction\_list>  
 RETURN

## Un programme en ROM

```
13 architecture rom_io of Prom64 is
14 type rom_array is array (NATURAL range <>) of std_logic_vector ( 63 downto 0 ) ;
15 constant rom : rom_array := (
16
17 x"0C00_0000_0010_200F"--0
18 x"A402_8804_8003_1400"--4
19 x"A002_22ff_2fff_C83E"--8
20 x"A401_1400_FFFF_FFFF"--C
21 x"200A_A007_8806_8007"--10
22 x"2008_C82C_200A_A007"--14
23 x"B008_8806_8007_C328"--18
24 x"1000_0000_0008_8806"--1C
25 x"A823_B008_0BF5_A000"--20
26 x"A000_8806_A823_B008"--24
27 x"0BE9_A000_1E00_FFFF"--28
28
29 );
```

## Suite

- instruction := <id\_Function>CALL!  
 <IP\_16\_BITS>!  
 IF <instruction\_list>  
 [ ELSE <instruction\_list> ]  
 ENDIF!  
 REPEAT <instruction\_list> {AGAIN !UNTIL}!  
 DO <instruction\_list> LOOP!  
 <Hexa\_constant> BR!  
 <Hexa\_constant> BNZ!  
 <Hexa\_constant> BZ!  
 <Hexa\_constant> BA!  
 HLT!  
 NILL!  
 NOP!  
 <Hexa\_constant> LIT

## Un assembleur post fixé

- A la Forth
- On utilise Forth pour écrire du Homade
- Installation de GNU Forth
- Un fichier de configuration
- Un fichier de code asm Homade
- Produit le code VHDL pour la ROM

## Exemples

```
variable read
program
read function
  f lit
  waitbtnPush
return
begin
repeat
  read call
.....
again
hit
endprogram
```